



ORIG: 03/2005 UPDATE: 12/2007 B.PALAN, I.HRUSKA CERN/ATLAS/TILECAL/LVPS: BURNIN TESTER MODULE FOR BURNIN\_TESTER  
 IN TOTAL, 8X BURNIN\_TESTER\_MODULES CONNECTED IN BURNIN\_TESTER. FIN REV. 07/2008 ADDED ON PCB BOARD: T2, R21, RA1, RA2 NOT SOLDERED COMPS ON PCB: BOTTOM SIDE R34, C30, C31, J17 CHANGED VALUES: R12=R13=10K AND NOT 100R; C30, C31 REPLACED BY SMD RES 3K3