



R48, R47, R46, R45, R44, R43, R42, R41, R40, R39, R38, R37, R36, R34, R33, R27 NOT SOLDERED ON FINAL PCB BOARD

ORIG: 03/2005 REVISED: 12/2007, 07/2008 B.PALAN, I.HRUSKA CERN/ATLAS/TILECAL/LVPS: BURNIN TESTER LOAD READOUT BOARD IN TOTAL 2X BOARDS ARE USED IN EACH BURNIN_TESTER